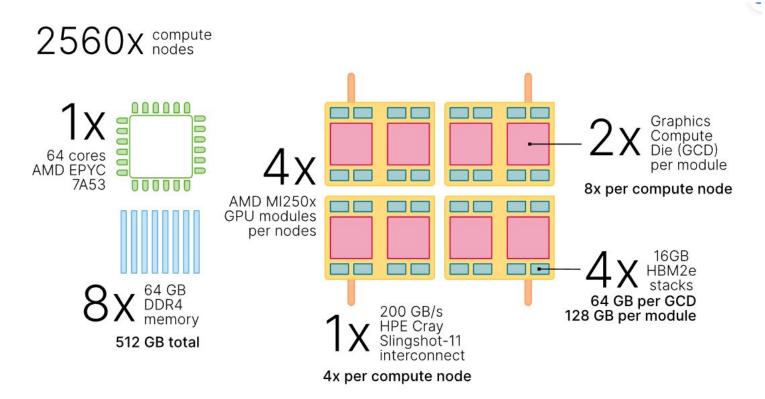
LUMI User Support Team

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Compute environment

- Hardware overview
- User environment
- Software on LUMI
- Development
- Storage

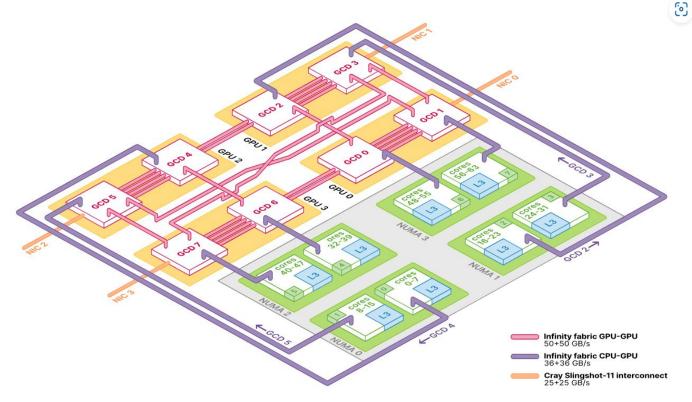
GPU nodes - LUMI-G



LUMI-G compute node

- 4 AMD MI250X GPUs based on the 2nd Gen AMD CDNA architecture
 - MI250x GPU is a multi-chip module with two Graphics Compute Dies (GCD)
 - 64 GB slice of HBM memory per 110 compute units (CU), 220 CUs and 128 GB total memory per module
 - LUMI-G nodes can be considered as **8 GPUs** nodes
- 64-core AMD EPYC 7A53 "Trento" CPU
 - "Zen 3" compute cores AVX2 256-bit instructions, maximum throughput of 16 double precision FLOP/s
 - 32 KiB of private L1 cache, a 32 KiB instruction cache, and 512 KiB of L2 cache per core
 - 32 MiB L3 cache shared between the group of eight cores
 - 256 MiB of L3 cache per processor.
 - CPU is configured as **4 NUMA nodes** (NPS4)
- 128 GiB of DDR4 memory per NUMA node for a total of **512 GiB** CPU memory

GPU-centric HW design



LUMI

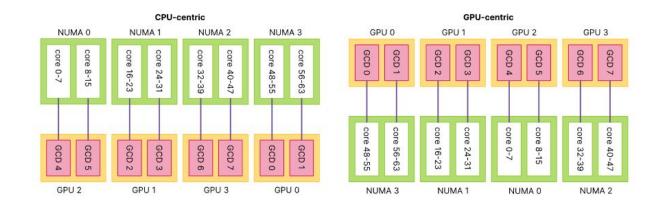
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- 5 links per GCD
- Two GCDs in the module use in-package "Infinity Fabric" interface with theoretical peak bidirectional bandwidth of up to **400 GB/s**
- GCDs on different modules are linked with either a single or double "Infinity Fabric" link, with a peak bidirectional bandwidth of **100 GB/s** and **200 GB/s**, respectively
- MI250x module is directly connected to the slingshot 11 network providing up to up to 25+25 GB/s peak bandwidth

NUMA binding

Proper binding the NUMA node to the GPU is crucial for achieving optimal performance



Source https://docs.lumi-supercomputer.eu/hardware/lumig/

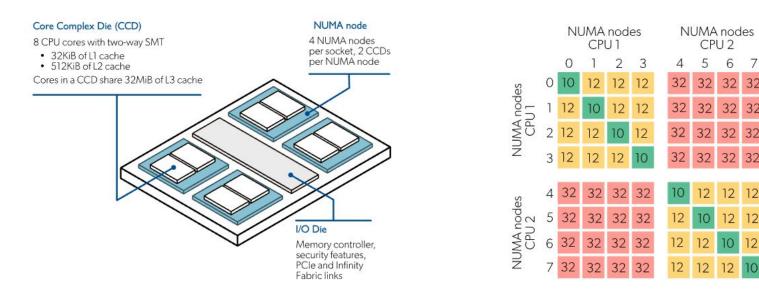
CPU nodes - LUMI-C

- **2x AMD EPYC 7763 CPUs** with 64 cores each running at 2.45 GHz
 - total of 128 cores per node
 - 2-way simultaneous multithreading (SMT), up to 256 threads per node
 - "Zen 3" compute cores, AVX2 256-bit vector instructions for a maximum throughput of 16 double precision FLOP/clock (AVX2 FMA operations)
 - 32 KiB of private L1 cache, a 32 KiB instruction cache, and 512 KiB of L2 cache
 - 8x core complex dies (CCDs) 8 cores each
 - L3 cache is shared between the eight cores of a CCD and has a capacity of 32 MiB for a total 256 MiB of L3 cache per processor

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CPU node

The LUMI-C compute nodes are configured with 4 NUMA zones ٠ ("quadrant mode") with 2 CCDs per quadrant



Network and interconnect

- All LUMI compute nodes uses HPE Cray Slingshot-11 200 Gbps network
- LUMI-C (CPU nodes) are equipped with a single endpoints (NIC)
- LUMI-G nodes (GPU nodes) have 4 endpoints one for each AMD MI250x GPU module
- Each endpoints provide up to 50 GB/s of bidirectional bandwidth
- The HPE Cray Slingshot NIC features high performance RDMA and hardware acceleration for MPI and SHMEM based software.

User Environment

- **Operating System**: Linux (Cray Operating System)
- **Remote access**: SSH, web interface (remote desktop), cloud interface (kubernetes)
- **Resource management**: SLURM
- **Software**: module environment, software stacks
- **User applications**: build via EasyBuild (and Spack)
- HPC Containers: singularity, bring your own container (limited support)
- **Development**: Cray Programming Environment

Software stacks

- CrayEnv offers the Cray Programming Environment (PE) and allows one to use it completely in the way intended by HPE-Cray. The environment also offers a limited selection of additional tools, often in updated versions compared to what SUSE Linux, the basis of the Cray Linux environment, offers. If you need a richer environment, you should use our other software stacks.
- LUMI/yy.mm is an extensible software stack that is mostly managed through EasyBuild. Each version of the LUMI software stack is based on the version of the Cray Programming Environment with the same version number.
- Spack

LUM

Software library

LUMI Software Library

Q, Search

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(becode and	License information
GROMACS	User documentation
SKOMACS	A note about the GPU weekons.
excentilitation	A note about the CPU weaknes with PUINED after the March/April 2023 system mainterance/update
License mornadon	User-installable modules (and
GROMACS is Free Software, available under the GNU Lesser General Public License (USPL), version 2.1. You can redistribute it and/or modify it under the terms of the LGPL as published by the Free	EasyConfigal
Software Foundation, either version 2.1 of the License, or lat your option(any later version.	Technical documentation
See the "About it" page on the GROMACS web also.	GROWAES and PLUMPE
	t.ooytia lict
	Version 2020.6 for CPE 21.08
User documentation	Version 2021.3 for CPI 21.08
A note about the GPU versions.	Version 2020.4 with
There exist two different versions of GROWINCS for AND CPUs.	PLUMED 2.6.4 for CPE 21.04
The authors of GROWICS use SYCL for an implementation for AVD GPUs. These versions are or will be part of the official GROWICS distribution channels.	Version 2021.5 for CP1 21.12
AMD has made a hip port of the CUDA version. It is not clear though to what estant that branch will see further development as GROMACS evolves.	Version 2021.4 with PLUMFD 2.7.4 and 2.8.0
When testing in early 2023, the HIP port offered significantly better performance than the SPCL port.	for CIV 21.12 and blive
Ones https://www.ananowitation.of/lw/EasyConfigs.to/find-out-on-which/Easted-of/GROMACS/the-recipes.are/based. That documentation is more towards the bottom of the page.	GROMACS-2023-dav- cpxGNU 22:08 MPI GPU
A note about the CPU versions with PLUMED after the March / April 2023 system maintenance / undate	Archived EasyConfigs

After the March April 2023 system update building PUMID brokes to those easystorings were replaced with two versions, one without Python support and one with a different way of enabling support for the caregogenetic modules. Therefore the corresponding EasyConfigs of GROMACS have also been replaced.

It is done this way because it is also unclear if the Python support is needed when used with GROMACS. Both versions with and without Python support for PLUMED blid,

See also the page on PLUMED.

User-installable modules (and EasyConfigs)

Install with the EasyBuild user module:

nb coasysselfigs -r

In access module help after installation and get reminded for which stacks and partitions the module is installed, use motule space- GROMACS/rearsaceo

EasyConfig.

IsoCardy GRIMAES 2011-466/2012 BFUNDE 22-44-by pt/ora 3.0122 CPU Lab. wHite BRIMCS/2021 4 upc/ora 2018 FUNDE 22-44-by pt/ora 3.0122 CPU BRIME 22-46-by pt/ora 3.0122 CPU BRIME 22-66-by p

https://lumi-supercomputer.git hub.io/LUMI-EasyBuild-docs/

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Technical documentation

Installing software

• **EasyBuild** - main channel for software provisioning, central installation (Software stack), custom installation (Contributed stack), recipes available in the software library (and GitHub)

• **Spack** - alternative tool for building entire environments

• **Containers** - last resort for complicated, complex environments (such as Conda)

Code development

- Cray Programming Environment (CPE) stack
 - Compiler suites (currently supported): Cray (CCE), GCC, AMD (ROCm)
 - Low-level GPU programming: **HIP** (ROCm), SYCL
 - High-level GPU programming: **OpenMP**, OpenACC
 - Compiler wrappers provide unified compiling environment
 - Libraries: CrayMPICH, Cray LibSci (numerical primitives), AMD GPU libraries
- Productivity tools
 - Parallel debuggers
 - Performance Analysis tools

GPU programming

- Code enabling scenarios for GPUs:
 - Translation from CUDA: "hipification"
 - OpenMP GPU Offload (recommended)
 - ML/DL frameworks: TensorFlow, PyTorch already supported
 - Use portable frameworks: Kokkos, Raja, Alpaka, others (support for AMD GPU is extending)

Storage Environment

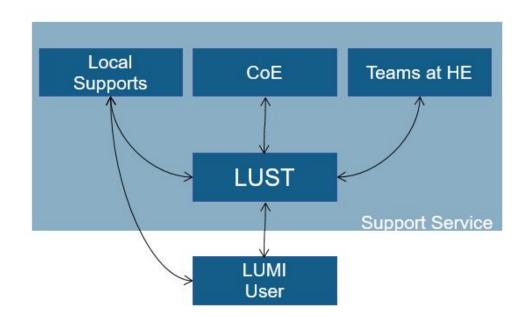
- LUMI-P: main storage
 - 4 independent Lustre file systems (20PB each)
 - Aggregate bandwidth of 240 GB/s
 - User and project directories
 - Scratch space
- LUMI-F: fast storage
 - Lustre file system with a storage capacity of 7 PB
 - Aggregate bandwidth of 1 740 GB/s
- LUMI-O: object storage
 - offers a total of 30 PB storage for storing, sharing, and staging of data

Getting support

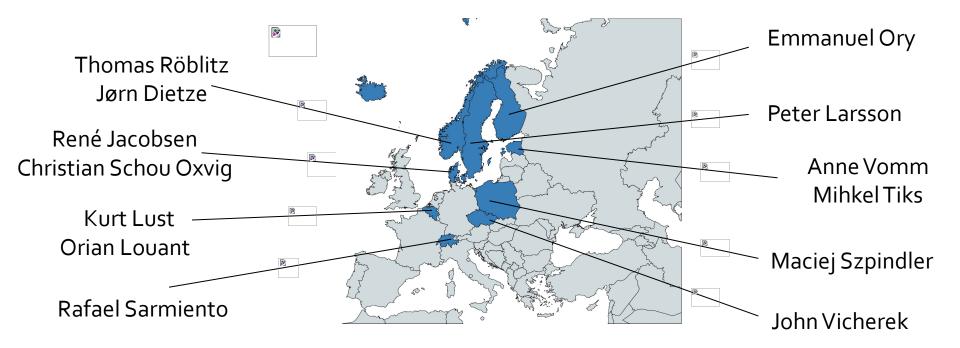
- Help desk
- LUMI user documentation
- Support web pages
- Software stack
- Porting and optimization
- User training
- Application Readiness Program

Support overview

 LUMI user support and a centralized help-desk by the distributed LUMI User Support Team



The LUMI User Support Team



Trainings

- Hackathon CSC April 17-20, 2023
- On-site 4day EE (May 3oth June 2nd, Tallin)
- On-line 1day May 9th, 2023 ; May 16th, 2023
- On-site 4day PL (preliminary first week of September, Warsaw)
- Hackathon PL (preliminary first week of December, Kraków)
- visit:

https://www.lumi-supercomputer.eu/events/

Project's support Options

- "Level 3" support (e.g. application enabling, methodology support) via local centers as well as the EuroHPC Competence Centers
- "Code Readiness" Call (2023, TBA)

Communication

- Support web pages
 - <u>https://lumi-supercomputer.eu/user-support/</u>
- Newsletter
 - <u>https://lumi-supercomputer.eu/newsletter/</u>
- FAQ
 - <u>https://lumi-supercomputer.eu/faq/</u>
- Help desk
 - <u>https://lumi-supercomputer.eu/user-support/need-help/</u>
- IM Chat
 - For registered users

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EuroHPC

